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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

P.O. Box 1450, Alexandria, VA 22313-1450

Applicant(s)

Allen et al.

Docket No.:

YOR920030175US1

Serial No.:

10/661,041

Filing Date:

September 12, 2003

Group:

2811

Examiner:

Cuong Q. Nguyen

Title:

Techniques for Patterning Features in Semiconductor Devices

Signature:

TRANSMITTAL LETTER

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith is a Response to Restriction Requirement relating to the aboveidentified patent application. There is no additional claim fee due in connection with the Response.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. Duplicate copies of this letter are enclosed.

Respectfully submitted,

Dated:

July 16, 2004

Michael J. Chang

Attorney for Applicant(s)

Reg. No. 46,611

Ryan, Mason & Lewis, LLP 1300 Post Road, Suite 205

Fairfield, CT 06824 (203) 255-6560



Docket No. YOR920030175US1

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Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the outstanding Office Action dated June 24, 2004 in the above-identified 25 application, Applicants submit the following remarks.